

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	158	((transmission adj1 gate\$1) and intel-\$.as.)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 11:37
S2	9	S1 and (low\$3 near4 leak\$4)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 11:38
S3	31469	hi-z hi\$z (high\$z high-z (high adj1 z)) (high adj1 impedance) high\$impedence tristat\$3 tri\$stat\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:11
S4	974	S3 near6 power\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 12:58
S5	23	S4 same (transmission adj1 gate\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 12:59
S6	334	S3 near6 power\$3 near6 (output input)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:04
S7	7	S6 same (transmission adj1 gate\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 12:59
S8	16	S5 not S7	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:00
S9	224	(S3 near6 power\$3 near6 (output input)) same (ic circuit chip)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:12
S10	35	S9 and g06f\$	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:05
S11	289	(hi-z hi\$z (high\$z high-z (high adj1 z)) (high adj1 impedance) high\$impedence tristat\$3 tri\$stat\$3) near4 forc\$3	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:12

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S12	8	S11 with power\$3 with (output input)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:15
S13	488	S3 with (power adj2 (supply regulator))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:16
S14	194	S13 same gate\$1	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:16
S15	11	S14 and g06f\$	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:19
S16	8	S13 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/14 13:19
S17	34174	(mov\$3 store\$1 storing) near6 (state) near6 (storage\$1 memory nvram)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:01
S18	506492	(disabl\$3 (power\$3 near2 (off down)) (low adj1 power) low\$power shut\$down ((sleep\$3 stand\$by) near2 (mode state)))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:03
S19	1812	S17 same S18	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 14:58
S20	178	S19 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 14:58
S21	17019	S18 with (external\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 14:59
S22	66	S17 same S21	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 14:59
S23	24	S22 and g06f\$	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:00

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S24	11	S22 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:00
S25	3060	((mov\$3 store\$1 storing) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:53
S26	489203	((disabl\$ low power\$3 ) near3 (off down)) (low adj1 power) low\$power shut\$down sleep\$3 stand\$by	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:04
S27	91	S26 with S25	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:07
S28	31	S27 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:07
S29	3060	((mov\$3 store\$1 storing) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:07
S30	91	S26 with S29	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:07
S31	31	S30 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:07
S32	39	((mov\$3 store\$1 storing) near6 (state)) with ((on\$die on\$chip) near6 (storage\$1 memory nvram)) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:54
S33	1	S32 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:55
S34	39	((mov\$3 store\$1 storing) near6 (state)) with ((on\$die on\$chip) near6 (storage\$1 memory nvram)) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:54

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S35	1	S34 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/25 15:55
S38	39	((mov\$3 store\$1 storing) near6 (state)) with ((on\$die on\$chip) near6 (storage\$1 memory nvram)) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:36
S39	1	S38 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:42
S40	3064	((mov\$3 store\$1 storing) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:37
S41	94	S40 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:44
S42	489881	((disabl\$ low power\$3 ) near3 (off down)) (low adj1 power) low\$power shut\$down sleep\$3 stand\$by	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:43
S44	660451	((disabl\$ low power\$3 ) near3 (off down)) (low adj1 power) low\$power shut\$down sleep\$3 stand\$by (power\$3 near2 (manag\$3 management\$1 savi\$3 conserv\$3 conservation control\$4))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:44
S45	211	S40 same S44	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:44
S46	61	S45 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 13:44
S47	3064	((mov\$3 store\$1 storing) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:30
S48	489881	((disabl\$ low power\$3 ) near3 (off down)) (low adj1 power) low\$power shut\$down sleep\$3 stand\$by	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 16:49

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S49	163	S47 same S48	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:32
S50	43	S49 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:33
S51	3117	((mov\$3 store\$1 storing) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor soc)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:24
S52	163	S51 same S48	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:24
S53	43	S52 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:32
S54	12	S52 and (sony\$.as. palm\$.as. microsoft\$.as. intel\$.as.)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:26
S55	3174	((mov\$3 store\$1 storing retain\$3 retention) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:32
S56	3227	((mov\$3 store\$1 storing retain\$3 retention) near6 (state)) with (storage\$1 memory nvram) with (ic (integrated adj1 circuit) chip processor soc)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:32
S57	6245	((((mov\$3 store\$1 storing retain\$3 retention) near6 (state)) with (storage\$1 memory nvram)) same (ic (integrated adj1 circuit) chip processor soc)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:43
S58	338	S57 same S48	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:44
S59	65	S58 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:44

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S60	1861	((mov\$3 store\$1 storing retain\$3 retention) near6 (state)) with (storage\$1 memory nvram)) same (ic (integrated adj1 circuit) chip soc)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:53
S61	136	S60 same S48	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:54
S62	21	S61 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:54
S63	1861	((mov\$3 store\$1 storing retain\$3 retention) near6 (state)) with (storage\$1 memory nvram)) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:54
S64	136	S63 same S48	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 17:54
S65	21	S64 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 18:08
S66	20	x\$scale\$1 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/26 18:08
S67	2351	((first second another 2nd 1st separate) near3 (pin\$1 terminal\$1 pad\$1 connector\$1 connector\$1)) with (power\$3 voltage\$1 v1 v2 "v.sub. 1" "v.sub.2") with ((active full low\$power ) near2 (mode\$1 state\$1) ((disabl\$ low power\$3 ) near3 (off down)) (low adj1 power) low\$power shut\$down sleep\$3 stand\$by)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:51
S68	258	(S67) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:53
S69	17	S68 and g06f\$	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:01
S70	11	S68 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:54

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S71	90	S67 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:44
S72	2	S71 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:46
S73	13	S71 and (intel-\$.as. advanced-mic\$. as. international-bus\$.as. sun-micro\$. as. honeywell-\$.as.)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:48
S74	11	S73 not S72	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:48
S75	655	((first second another 2nd 1st separate) near3 (pin\$1 terminal\$1 pad\$1 connector\$1 connector\$1)) with (power\$3) with (stand\$by low\$power (low adj1 power) (stand adj1 by) sleep\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:56
S76	77	(S75) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:57
S77	6	S76 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:58
S78	658	((first second another 2nd 1st separate) near3 (pin\$1 terminal\$1 pad\$1 connector\$1 connector\$1)) with (power\$3 vdd "v.sub.dd" vstby vstandby "v.sub.standby") with (stand\$by low\$power (low adj1 power) (stand adj1 by) sleep\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:57
S79	77	(S78) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:57
S80	6	S79 and 713/300-340.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 10:58
S81	3144	(mux\$3 multi\$plex\$4 select\$3 selectiv\$3) with (external\$3 near2 power\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:51

## EAST Search History

S82	32	S81 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:38
S83	1800	(mux\$3 multi\$plex\$4 selector\$1 selector\$1 selecting selectiv\$3) with (external\$3 near2 power\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:53
S84	16	S83 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:54
S85	16	(mux\$3 multi\$plex\$4 select\$3 selectiv\$3) with (external\$3 near2 power\$3) with feedback	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:53
S86	490	((mux\$3 multi\$plex\$4 selector\$1 selector\$1 selectiv\$3) with (external\$3 near2 power\$3)).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:55
S87	67	((((mux\$3 multi\$plex\$4 selector\$1 selector\$1 selectiv\$3) with (external\$3 near2 power\$3)) and (ic (integrated adj1 circuit) chip soc system\$on\$chip))).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:56
S88	32	((((mux\$3 multi\$plex\$4 selector\$1 selector\$1 selectiv\$3) with (external\$3 near2 power\$3)) same (ic (integrated adj1 circuit) chip soc system\$on\$chip))).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 16:58
S89	1	((((mux\$3 multi\$plex\$4 selector\$1 selector\$1) with (external\$3 near2 power\$3)) same (ic (integrated adj1 circuit) chip soc system\$on\$chip))). clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 17:00
S90	37	((((mux\$3 multi\$plex\$4 selector\$1 selector\$1) with (external\$3 near2 power\$3)) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 17:01
S91	27	((((mux\$3 multi\$plex\$4 ) with (external\$3 near2 power\$3)) same (ic (integrated adj1 circuit) chip soc system\$on\$chip)))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/27 17:01
S92	0	("(((select\$4choos\$4)near3power\$4)w ithmultiplex\$4)samefeedback").PN.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 14:59

## EAST Search History

S93	0	("(((select\$4choos\$4)near3power\$4)w ithmultiplex\$4)samefeedback").PN.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:15
S94	0	("(((select\$4choos\$4)near3power\$4)w ithmultiplex\$4)samefeedback").PN.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:15
S95	11	((select\$4 or choos\$4) near3 power\$4) with multiplex\$4) same feedback	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:41
S96	14	((select\$4 or choos\$4) near3 power\$4) with (multiplex\$4 mux muxing muxed)) same feedback	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:42
S97	3	S96 not S95	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:42
S98	1673	(power adj1 suppl\$4) near8 (multiplex\$4 mux muxing muxed)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:00
S99	17	S98 with feedback	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 15:54
S10 0	17	(S98 same feedback) not S99	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:00
S10 1	161429	((power\$4 (power adj1 suppl\$4)) near2 drive\$2)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:19
S10 2	6876	((power\$4 (power adj1 suppl\$4)) near2 drive\$2) with (transistor\$1)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:42
S10 3	25	S102 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:46
S10 4	836	S102 with (mos cmos)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:47

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S10 5	1	S104 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:46
S10 6	1241	S102 same (mos cmos)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:46
S10 7	3	S106 and intel-\$.as.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:46
S10 8	1	S104 same ((hi-z hi\$z (high\$z high-z (high adj1 z)) (high adj1 impedance) high\$impedence tristat\$3 tri\$stat\$3) near4 forc\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:50
S10 9	3	S104 same ((hi-z hi\$z (high\$z high-z (high adj1 z)) (high adj1 impedance) high\$impedence tristat\$3 tri\$stat\$3))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 16:51
S11 0	1	"6484265".pn.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 17:26
S11 1	1	"6754692".pn.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 17:29
S11 2	1	"6574577".pn.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/29 17:50